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Sheet 1

Filing Date April 27, 2005

First Named Inventor Jun-ichi Okamura

Art Unit Genfirm: No. 5031 2816

Examiner Name DIANA CHENG
Attorney Docket Number 1032-0002WOUS

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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U. S. PATENT DOCUMENTS Pages, Columns, Lines, Where Name of Patentee or Publication Date MM-DD-YYYY Examiner Cite No. **Document Number** Relevant Passages or Relevant Applicant of Cited Document Initials' Figures Appear Number-Kind Code<sup>2 (# known)</sup> /DC/ <sup>US-</sup> 5,045,806 09-03-1991 Yan 7DC/ <sup>US-</sup> 5,999,028 12-07-1999 Knoch et al. US. US-US US-US. US-US-US. US-US-US-US. US.

		FORE	IGN PATENT DOCL	IMENTS		
Examiner Initials*	Cite No.'	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages	
		Country Code <sup>3</sup> "Number <sup>4</sup> "Kind Code <sup>6</sup> (if known)	MM-DD-YYYY		Or Relevant Figures Appear	T
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Substitute for form 1449/FTO	Application Number	10/533,141			
INFORMATION DISCLOSURE	Filing Date	April 27, 2005			
STATEMENT BY APPLICANT	First Named Inventor	Jun-ichi Okamura			
	Art Unit	Genfirmation No. 5031 28/6			
(Use as many sheets as necessary)	Examiner Name	DIANA CHENG			

Attorney Docket Number

1032-0002WOUS

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/DC/		James H. Atherton and H. Thomas Simmonds, "An Offset Reduction Technique for Use with CMOS Integrated Comparators and Amplifiers", 8107 IEEE, 08/27/92; New York, US	
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Co	nplete if Known
Application Number	U/222141
Filing Date	
First Named Inventor	Jun-ichi Okamura
Art Unit	2816
Examiner Name	Diana Cheng
Attorney Docket Number	1032-0002WOUS

Examiner Initials*	Cite No.1	U. S. PATEN  Document Number Publication Date		Name of Patentee or	Pages, Columns, Lines, Where	
		Number-Kind Code <sup>2 (# known)</sup>	MM-DD-YYYY	Applicant of Cited Document	Pages, Columns, Unes, Where Relevant Passages or Relevant Figures Appear	
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